

Surface Mount Transient Voltage Suppressor Diode

Features

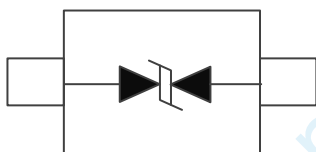
- ✧ ESD protection of one Dataline
- ✧ Solid-state silicon-avalanche technology
- ✧ Working Voltage: 5.0V
- ✧ Peak Power up to 42 Watts @ 8 x 20 μ s Pulse
- ✧ Low Leakage current
- ✧ Response Time is Typically < 1 ns
- ✧ Low capacitance (Cj=3.1pF typ.)
- ✧ IEC 61000-4-2 (ESD) $\pm$ 25kV (air), $\pm$ 25kV (contact)
- ✧ IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- ✧ Cellular Handsets & Accessories
- ✧ Portable devices
- ✧ Dataline
- ✧ Computer & Notebook

Mechanical Data

- ✧ UL Flame Rating : UL94V-0
- ✧ Reel Size : 7 inch
- ✧ RoHS/WEEE Compliant



Circuit Diagram



SOD323

Absolute Maximum Rating at $T_A = 25^\circ\text{C}$, unless otherwise specified

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu\text{s}$)	P_{PP}	42	Watts
Peak Pulse Current ($t_p = 8/20\mu\text{s}$) (note1)	I_{pp}	3.0	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	± 25 ± 25	kV
Lead Soldering Temperature	T_L	260(10seconds)	$^\circ\text{C}$
Junction Temperature	T_J	-55 to + 125	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to + 150	$^\circ\text{C}$

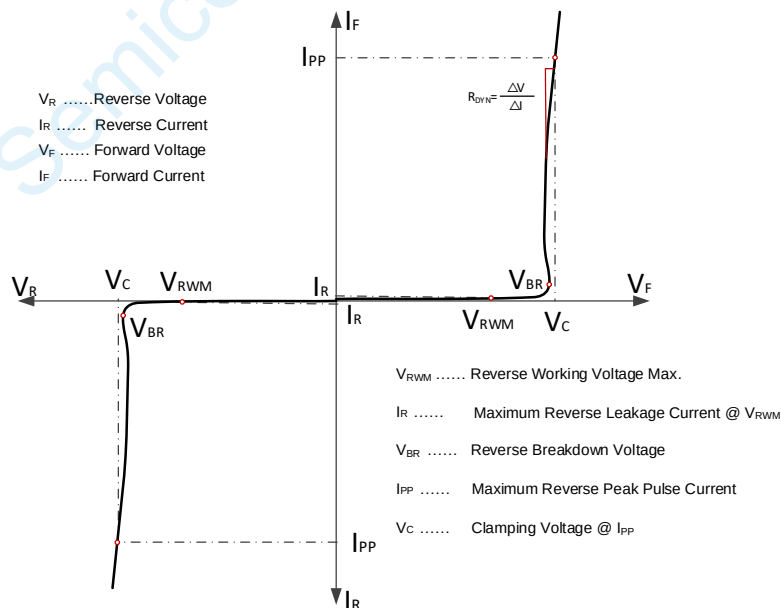
Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typical	Max.	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1\text{mA}$	6.0			V
Reverse Leakage Current	I_R	$V_{RWM}=5.0\text{V}$, $T=25^\circ\text{C}$			1.0	μA
Peak Pulse Current	I_{PP}	$t_p=8/20\mu\text{s}$			3.0	A
Clamping Voltage	V_C	$I_{PP}=1\text{A}$, $t_p=8/20\mu\text{s}$		7.8		V
Clamping Voltage	V_C	$I_{PP}=3.0\text{A}$, $t_p=8/20\mu\text{s}$		11	14	V
ESD Clamping Voltage	V_C	$t_p=0.2/100\text{ns}$, $I_{TLP}=4\text{A}$		7.3		V
ESD Clamping Voltage	V_C	$t_p=0.2/100\text{ns}$, $I_{TLP}=16\text{A}$		11.1		V
Dynamic Resistance	R_{DYN}	$t_p=0.2/100\text{ns}$		0.32		Ω
Junction Capacitance	C_J	$V_R=0\text{V}$, $f=1\text{MHz}$		3.1	4.8	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

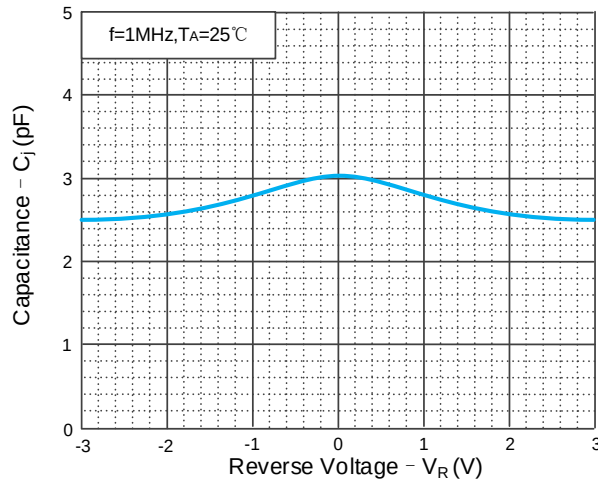
1. All parameters specified at $T_A = 25^\circ\text{C}$ unless otherwise noted.
2. These parameters are guaranteed by design and characterization.
3. Standard IEC 61000-4-2 with $C_{Discharge} = 150\text{ pF}$, $R_{Discharge} = 330\ \Omega$

Electrical Parameters at $T_A = 25^\circ\text{C}$, unless otherwise specified

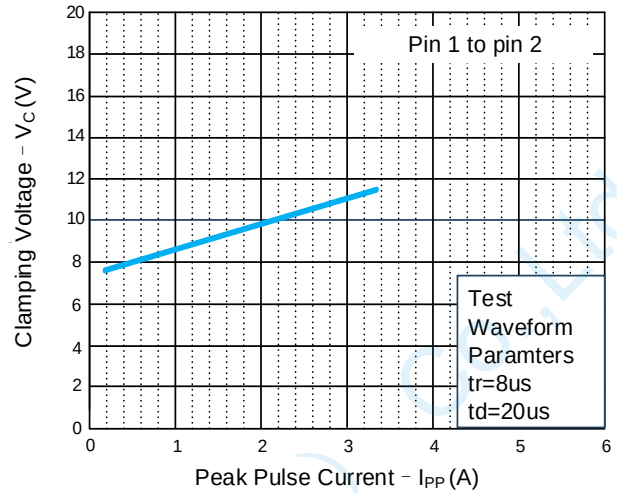


Typical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

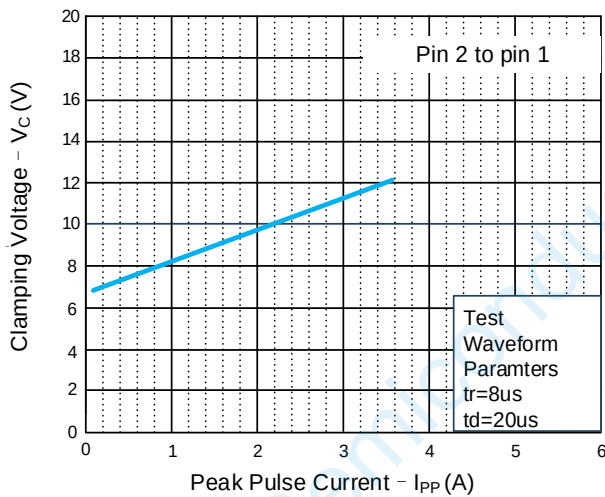
Capacitance vs. Reverse Voltage



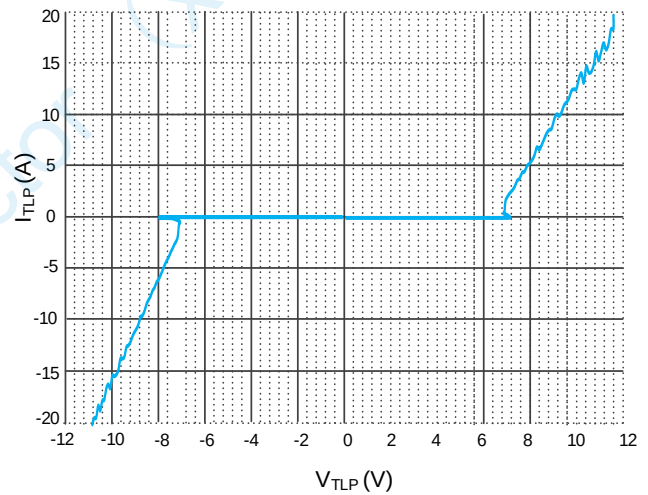
Clamping Voltage vs. Peak Pulse Current



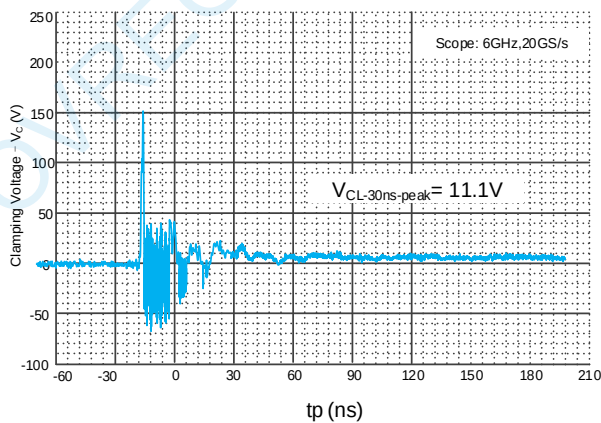
Clamping Voltage vs. peak PulseCurrent



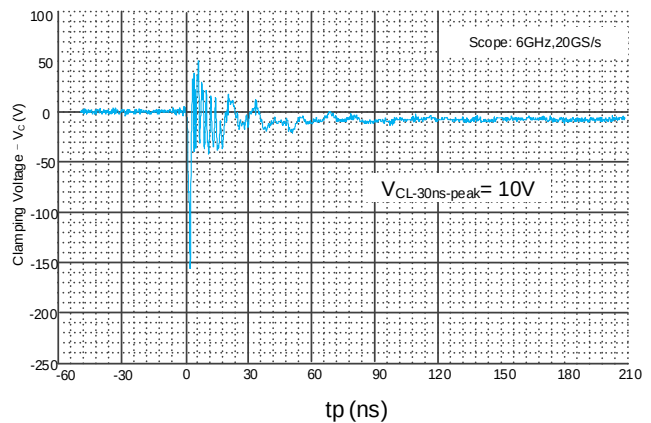
TLP Measurement



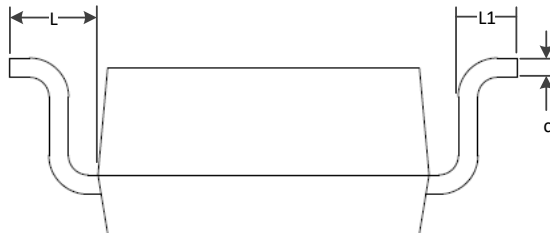
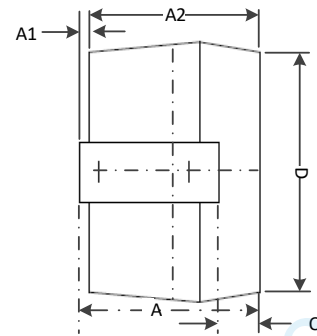
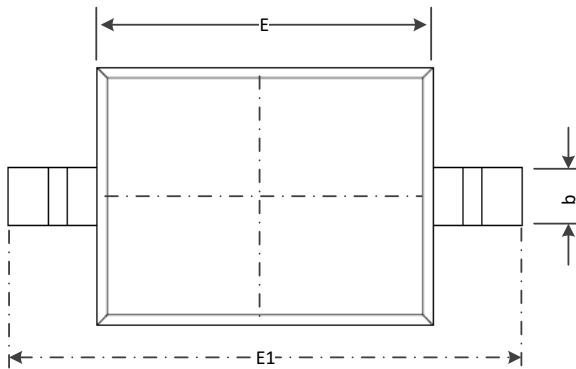
IEC61000-4-2 :15 kV positive pulse from pin 1 to pin 2



IEC61000-4-2 :15 kV negative pulse from pin 1 to pin 2

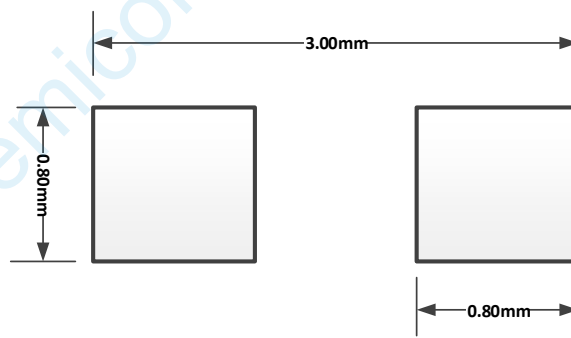


Package information



COMMON DIMENSION (MM)			
PKG	SOD323		
Symbol	MIN.	NOM.	MAX.
A	0.800	0.900	1.000
A1	0.000	0.050	0.100
A2	0.800	0.850	0.900
b	0.250	0.300	0.350
c	0.080	-	0.150
D	1.200	1.300	1.400
E	1.600	1.700	1.800
E1	2.500	2.600	2.700
L	0.475 REF		
L1	0.250	0.260	0.270
Q	0.250 REF		

Recommend PCB Layout



Notes: This PCB Layout Is For Reference Purposes Only

Marking



Ordering Information

Part number	Material	Type	Reel size	MOQ	MOQ/internal box	MOQ/carton
OVE1472A1	GREEN	T/R	7 inch	3k/reel	10reel= 30k/box	4boxes=120k/carton