Surface Mount Transient Voltage Suppressor Diode

Features

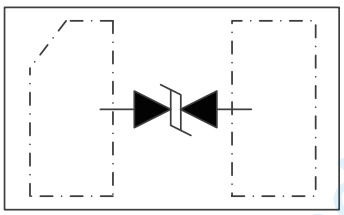
- ✧ Bi-directional ESD protection of one line
- ✧ Solid-state silicon-avalanche technology
- ✧ Working Voltage: 5.0V
- ✧ Peak Power up to 52 Watts @ 8 x 20μs Pulse
- ✧ Low Leakage current
- ✧ Response Time is Typically < 1 ns
- ✧ IEC 61000-4-2 (ESD) ±15kV (air), ±15kV (contact)
- ✧ IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- ✧ Cellular Handsets & Accessories
- ✧ Portable devices
- ✧ Computers and Peripherals
- ✧ Notebook

Mechanical Data

- ✧ UL Flame Rating : UL94V-0
- ✧ Reel Size : 7 inch
- ✧ RoHS/WEEE Compliant



Circuit Diagram



DFN1006

Absolute Maximum Rating at T_A = 25 °C, unless otherwise specified

Rating	Symbol	Value	Units
Peak Pulse Power (t_p =8/20μs)	P_{PP}	52	Watts
Peak Pulse Current (t_p =8/20μs) (note1)	I_{pp}	6.5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	±15 ±15	kV
Lead Soldering Temperature	T_L	260(10seconds)	℃
Junction Temperature	T_J	-55 to + 125	℃
Storage Temperature	T_{stg}	-55 to + 150	℃

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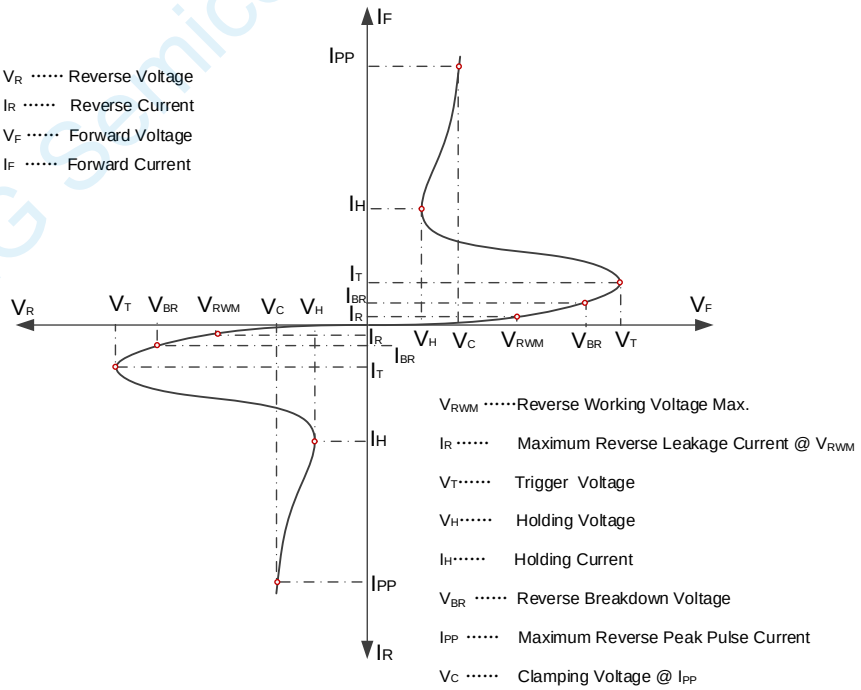
Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typical	Max.	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Trigger Voltage	V_T	I_T=20mA		16		V
Reverse Leakage Current	I_R	V_{RWM}=5V, T=25℃			0.5	μA
Holding Voltage	V_H	I= I_H		2.5		V
Holding Current	I_H	V= V_H		40		mA
Peak Pulse Current	I_{PP}	tp =8/20μs			6.5	A
Clamping Voltage	V_C	I_{PP}=1A, tp=8/20μs			3.8	V
Clamping Voltage	V_C	I_{PP}=6.5A, tp=8/20μs			8.0	V
ESD Clamping Voltage	V_C	tp=0.2/100ns,I_{TLP}=4A		5.2		V
ESD Clamping Voltage	V_C	tp=0.2/100ns,I_{TLP}=16A		7.1		V
Dynamic Resistance	R_{DYN}	tp=0.2/100ns		0.158		Ω
Junction Capacitance	C_J	V_R = 0V, f = 1MHz		0.60		pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
1. All parameters specified at T_A = 25°C unless otherwise noted.
2. These parameters are guaranteed by design and characterization.
3. Standard IEC 61000−4−2 with $C_{Discharge}$ = 150 pF, $R_{Discharge}$ = 330 Ω

Electrical Parameters at T_A = 25 °C, unless otherwise specified



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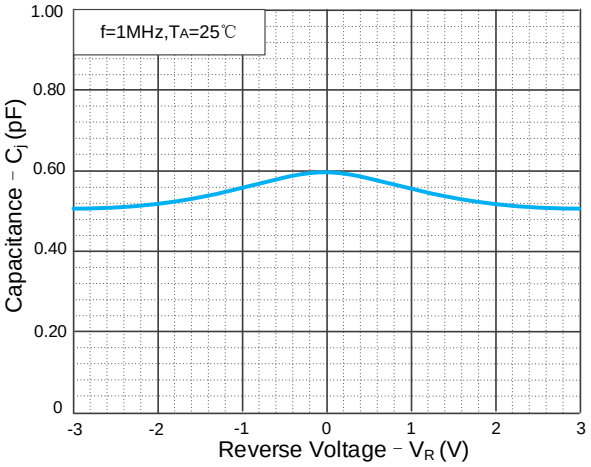
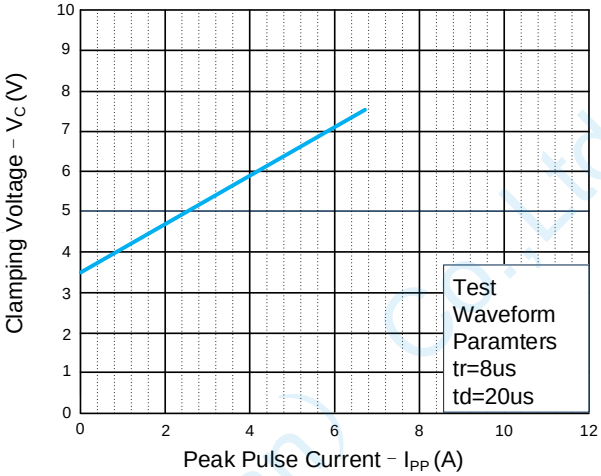
Typical Characteristics at T_A = 25 °C, unless otherwise specified

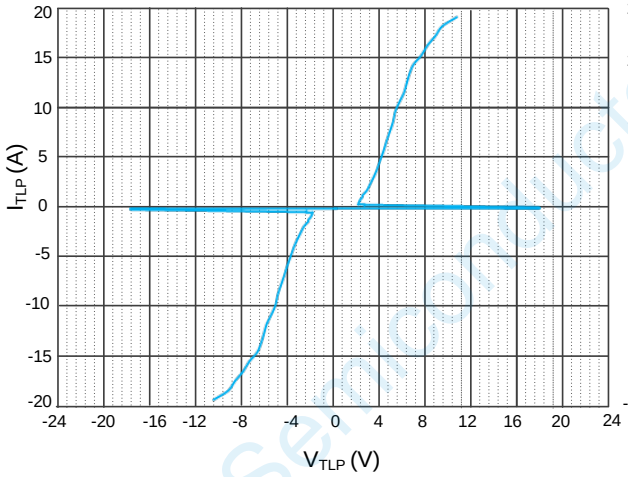
Capacitance vs. Reverse Voltage



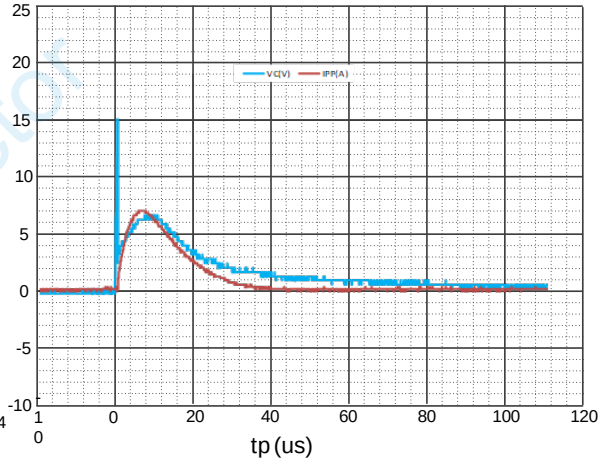
Reverse Clamping Voltage vs. Peak Pulse Current



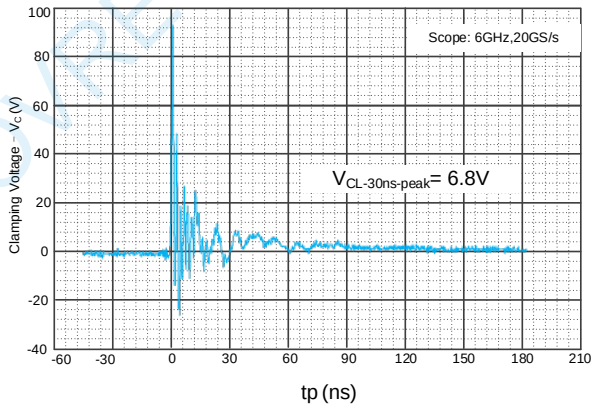
TLP Measurement



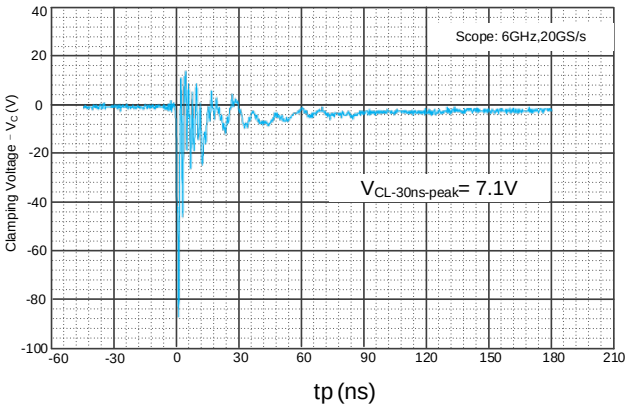
IEC61000-4-5 : 8/20us Waveform



IEC61000-4-2 :15 kV positive pulse from pin 1 to pin 2



IEC61000-4-2 :15 kV negative pulse from pin 1 to pin 2



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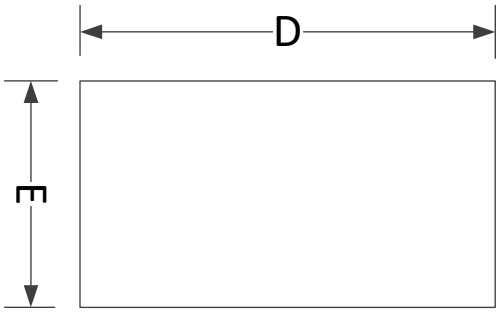
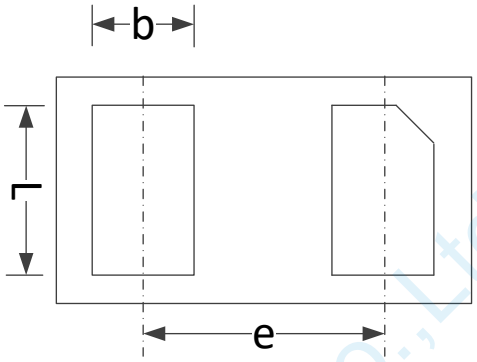
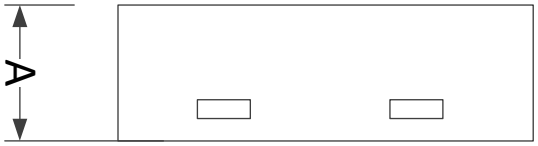
Package Information



TOP VIEW



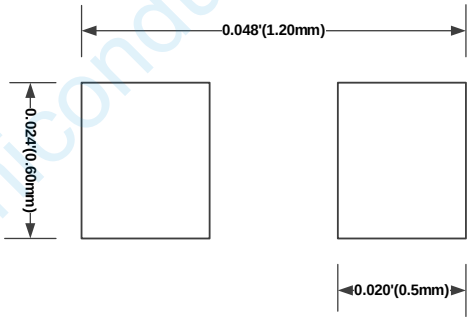
BOTTOM VIEW



SIDE VIEW

COMMON DIMENSION (MM)			
PKG	DFN1006		
REF.	MIN.	NOM.	MAX.
A	0.43	-	0.53
b	0.20	0.25	0.30
L	0.45	0.50	0.55
e	0.65REF		
D	0.95	1.00	1.05
E	0.55	0.60	0.65

Recommend PCB Layout



Notes: This PCB Layout Is For Reference Purposes Only

Marking

SD

Ordering Information

Part number	Material	Type	Reel size	MOQ	MOQ/internal box	MOQ/carton
OVEL732A1B	GREEN	T/R	7 inch	12k/reel	10reel= 120k/box	4boxes=480k/carton